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② Inventor: Matsunaga, Yoshiyuki

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1-4-21-212 Kobukuroya

Kamakura-shi, Kanagawa-ken(JP)

⑥ Designated Contracting States:
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④ Representative: Lehn, Werner, Dipl.-Ing. et al
Hoffmann, Eitle & Partner Patentanwälte
Arabellastrasse 4
W-8000 München 81(DE)

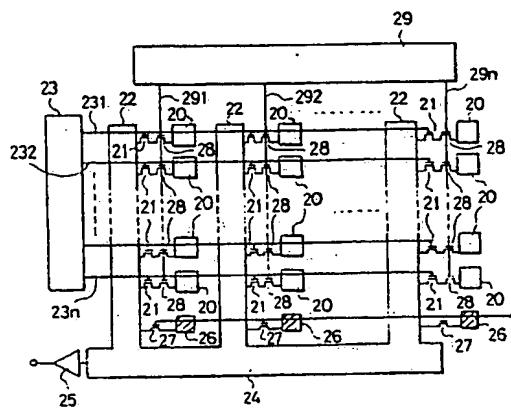
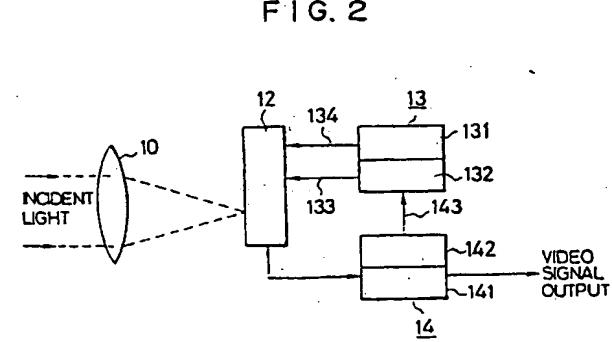
⑤ Applicant: KABUSHIKI KAISHA TOSHIBA
72, Horikawa-cho Sawai-ku
Kawasaki-shi Kanagawa-ken 210(JP)

④ Solid-state camera.

⑤ Disclosed is a solid-state camera comprising a solid-state image sensor element(12) composed of a plurality of planarily arranged units of picture element cells(20), drive means(13) for supplying a pulse power source thereto, signal process means(14) for obtaining a picture image signal by processing an output signal from each the unit of picture element cells(20), detection means(142) for generating a detection signal based on an output voltage value corresponding to the picture image signal, and sensitivity adjustment means(132) for generating a sensitivity adjustment signal to adjust sensitivity re-

quired based on the detection signal(133) in each the unit of picture element cells(20). This camera controls output signals for read transistors corresponding to the respective picture element cells(20) and the respective picture element cells(20) in a unit row at the same time, and adjusts an output voltage value from each the unit of picture element cells(20) based on an output voltage value of each unit of picture element cells(20) in a previous line, frame or field by stop signal registers for stopping output signals of the picture element cells(20).

FIG. 3



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SOLID-STATE CAMERA

BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates to a solid-state camera, and more particularly relates to a solid-state camera which can provide fine picture images even under a backlight state.

Description of the prior art

Fig.1 is a schematic diagram to show a composition of a solid-state camera or a video camera which is ordinarily used. In the same diagram, an incident light is focused by a lens 1 on an image sensor face of a solid-state image sensor or a solid-state area sensor 2 to form an image. The solid-state area sensor 2 is composed of a plurality of semiconductor photoelectric conversion elements (picture element cells) which are planarly arranged on the plane image picture face. The solid-state area sensor 2 is driven by a pulse power source and direct current outputted from a driven means 3 and photoelectric conversion signals of a photoimage focused on the image sensor face are successively output to a signal process means 4. And then electric signals as the photoelectric conversion signals from the solid-state area sensor 2 are processed and output as video signals by the process means 4.

When such a conventional general video camera is used under the backlight, it is usually difficult to effectively take fine picture images. For example, when the video camera is used for taking scenes outside the window immediately after photographing the inside of a room, very strong light rays from the outside come into the image faceplate of the solid-state area sensor 2.

In this case, since output of a portion corresponding to the light rays in the picture element cells composing the solid-state area sensor 2 is saturated, a picture image corresponding to the portion becomes white. Thus, it is difficult to clearly take scenes out of the window in such a case. To cope with the problem, an ordinary camera has an autoiris mechanism for automatically adjusting or closing an iris of the lens to control the incident light. Accordingly, the camera having such an autoiris mechanism can guarantee the photographing of scenes outside of the window. However, in contrary, the picture images in the room become dark and undistinguishable respectively. Namely, it is difficult to clearly take picture images of both the

outside and the inside of a room at the same time by such a camera.

5 SUMMARY OF THE INVENTION

Therefore, it is an object to provide a solid-state camera which can guarantee excellent picture images even a condition under backlight.

10 To achieve the above mentioned object, a solid-state camera which is a preferred embodiment of the present invention comprises a solid-state image sensor element composed of a plurality of picture element cells planarly arranged for outputting an electric signal obtained by photoelectric conversion of an incident light, drive means for driving the solid-state image sensor element, and signal process means for obtaining a picture image signal by processing an output signal from the solid-state image sensor element. Moreover, the signal process means has means for detecting a position of an incident light introduced onto the solid-state image sensor element in an amount larger than a predetermined amount and generating a detection signal, and the drive means has a sensitivity adjustment means for generating a sensitivity adjustment signal so as to control sensitivity of a unit of picture element cells in each specific portion in the solid-state image sensor element based on the detection signal.

15 In the above described solid-state camera of the embodiment, when strong incident light is received in a portion of the planar solid-state image sensor element, an output signal to be outputted from a picture element cell in the portion is saturated. Then, the signal process means generates the detection signal of position for specifying a position of the picture element cell. While, the drive means generates a sensitivity adjustment signal for adjusting the output of the picture element cell based on the detection signal corresponding to the position so as to control an output signal value from each specific unit of picture element cells. Namely, when an incident light is introduced onto a portion or the entire area of the solid-state image sensor element in an amount larger than a predetermined amount as in a case of photographing under a condition of backlight, sensitivity of the portion is automatically controlled. Thus, the solid-state camera can realize photographing a portion of a strong light, such as scenes outside the window, and another portion of a weak light, such as an interior of a room, at the same time or successively.

20 These and other objects, features and advantages of the present invention will be more appar-

ent from the following description of a preferred embodiment, taken in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig.1 is a block diagram to show schematical composition of a conventional solid-state camera,

Fig.2 is a block diagram to show schematical composition of an embodiment of the present invention,

Fig.3 is a compositional diagram of a line address type CCD sensor to be used in the solid-state camera of the embodiment shown in Fig. 2,

Fig.4 is a plan view of a line address type CCD sensor for one line in the compositional diagram of the line address type CCD sensor shown in Fig. 3,

Fig. 5A is a cross section taken along line X-X in the line address type CCD sensor for one line shown in Fig. 4, and Figs.5B to SD are potential diagrams to respectively show operational states thereof,

Fig.6 is a diagram to explain operation for stopping read in the line address type CCD sensor for one line shown in Fig.4, and

Fig.7 is a diagram to explain an operational range of sensitivity suppression in the line address type CCD sensor shown in Fig.4.

DETAILED DESCRIPTION OF THE EMBODIMENT

Hereinafter, an embodiment of the present invention will be described with reference to the drawings.

Fig.2 is a block diagram to show composition of a solid-state camera which is an embodiment of the present invention. In the same diagram, reference numeral 10 designates a lens for focusing an incident light to form an image on an image sensor face of a solid-state camera, that is, a solid-state image sensor element 12 (hereinafter called an area sensor). The area sensor 12 performs photoelectric conversion of the incident light introduced in the solid-state camera through the lens 10, then outputs a corresponding electric signal. Namely, the sensor 12 is a so-called semiconductor image sensor element. Particularly, the solid-state area sensor 12 of the embodiment is composed of units of picture element cells respectively having independent sensitivity adjustment mechanism as mentioned below.

While, reference numeral 13 denotes drive means for basically supplying a direct current (DC) power source and a pulse power source to the

solid-state area sensor 12.

Moreover, reference numeral 134 shows a control signal for driving the solid-state area sensor 12.

The drive means 13 includes a drive portion 131 and a sensitivity suppression pulse generation portion 132 for generating a sensitivity suppression pulse 133 to be outputted to the solid-state area sensor 12.

Reference numeral 14 designates signal process means for receiving the photoelectric conversion signal from the solid-state area sensor 12, then processing the signal so as to obtain a video signal. The signal-process means includes a signal process portion 141 and a position detection portion 142 for detecting a position on the solid-state area sensor 12, where a strong incident light is received. The positional information of each the picture element cell is detected by judging whether each output of the picture element cell is saturated.

The solid-state camera of the embodiment has such the composition as described above. For example, when a strong incident light is received in a portion of the image sensor face of the solid-state area sensor 12 and the picture element output in the portion is saturated, a position of each the picture element cell in the portion is detected by the position detection portion 142 of the signal process means 14, then a position signal 143 is inputted into the sensitivity suppression pulse generating portion 132 of the drive means 13. In the sensitivity suppression pulse generating portion 132, the position signal 143 is decoded to generate a pulse 133 for suppressing the sensitivity of each the picture element cell where the strong incident light is received so as to adjust it suitably. Accordingly, even when a strong incident light is introduced onto a portion of the solid-state area sensor 12 in a case under a condition of backlight, since the sensitivity of each the picture element cell in the portion is automatically suppressed, white blooming of a picture image to be formed corresponding to the portion can be prevented. Thus, the image can be formed clearly. Therefore, even in case of photographing scenes outside of the window and those in the room at the same time or successively, it becomes possible to form clear picture images of these. Incidentally, it is preferred the portion in the solid-state area sensor 12, whose sensitivity is to be suppressed, includes not only picture element cells where a strong incident light is directly received but also picture element cells in the vicinity of them. It is possible to carry out the process for suppressing the sensitivity by guessing from a signal of the one field or one frame, or to carry out it by guessing from a photoelectric conversion signal outputted from a unit of picture element cells before one line.

Moreover, it is necessary for each the unit of

picture element cells in the solid-state area sensor 12 to have a sensitivity adjustment function independently. An example of such an area sensor will be described next.

Fig.3 is a compositional diagram of a solid-state area sensor to be used in the solid-state camera of the embodiment. This sensor comprises a plurality of planarily arranged signal storage diodes 20, read transistors 21, vertical CCD 22 for respectively reading electric charges stored in the diodes 20 through the read transistors 21, a vertical shift register 23 for driving the read transistors 21, a horizontal CCD 24 for successively transferring signals outputted from the vertical CCD 22 to output means 25, drains 26 and exhaustion gates 27 for exhausting signal charges supplied to respective potential wells (to be described hereinafter) in the vertical CCD 22.

In the solid-state camera of the embodiment having the above composition, the vertical CCD 22 respectively have signal potential wells for transferring signal charges and empty potential wells for transferring unnecessary charges, for example such as smear charges. These wells transfer signal charges stored in the diodes 20 to the horizontal CCD 24 successively. Namely, to transfer the signal charges from the signal storage diodes 20 to the signal potential wells in the respective vertical CCD, the vertical shift register 23 is suitably driven to apply voltage of the H (high) level to respective address lines 231, 232, ..., 23n so as to make the transistors 21 "on". Thereby, the signal charges stored in the diodes 20 are read by the vertical CCD, and then transferred to the horizontal CCD 24 successively.

In the solid-state area sensor 22, it is necessary to change the sensitivity of each the planarily arranged unit of picture element cells corresponding to an amount of the incident light. As shown in the same diagram, stop gates 28 therefore are provided in parallel with the signal storage diodes 20 and the respective transistors 21 so as to partially stop exhaustion of the charges by setting the gates 28 "off" corresponding to picture element cells whose sensitivity should not be suppressed. The stop gates 28 are respectively driven by a stop signal register 29 through stop gate lines 291, 292, ..., 29n. Namely, with respect to the picture element cells on one line determined by the shift register 23, whose sensitivity should not be suppressed, a signal voltage is respectively applied to the corresponding gates 28 to make them off through the lines 291, 292, ..., 29n of the stop signal register 29 so as to partially stop exhaustion of the charges. Incidentally, information (a value of the level) to be given onto the lines 291, 292, ..., 29n is always renewed. Accordingly, in the CCD sensor as shown in Fig. 3, picture element cells

whose sensitivity should be suppressed can be selectively and independently designated in the planar arrangement. Of course, the area whose sensitivity should be suppressed comprises picture element cells whose output is saturated by a strong incident light as described above and other cells existing around them.

Fig.4 is a plan view of the vertical CCD for one line in the solid-state area sensor shown in Fig. 3. Fig. 5A is a cross section taken along line X-X of the vertical CCD for one line in Fig.4, and Figs.5B to SD show potential diagrams in respective timings of the vertical CCD for one line shown in Fig. 5A. In Fig. 5A, an n-type buried channel 32 of CCD is formed in the surface of a p-type silicon substrate 31 by ion implantation, and a plurality of transfer electrodes 34 to be driven by 4-phase clock pulses ϕ 1 to ϕ 4 are arranged adjacent to one another over the channel 32 through an insulating film 33.

Next, basical operation for reading the picture element cells planarily arranged will be explained.

The stop gate lines 291, 292, ..., 29n of the stop register 29 in Fig.3 are set at the H level to turn on these stop gates lines 291, 292, ..., 29n. Then, a read pulse is given to an address line, for example, the line 231, so that signal charges stored in the corresponding photodiodes 20 on the address line 231 are respectively read into the vertical CCD 22. Thereafter, the stop gate lines 291, 292, ..., 29n are set at the L level to turn off the gate lines 291, 292, ..., 29n. As the result, the signal charges for one line read in the vertical CCD 22 are respectively transferred into the horizontal CCD 24 by the clock pulses 1 to 4, then outputted to the signal process means 14 through the output portion 25.

Subsequently, the stop gate lines 291, 292, ..., 29n are set at the on state, and a read pulse is applied to the address line 232, so that signal charges stored in the corresponding photodiodes 20 on the address line 232 are respectively read into the vertical CCD 22. Then, the signal charges for one line read in the vertical CCD 22 are respectively transferred to the signal process means 14 through the horizontal CCD 24 and the output portion 25 in the same manner as described above.

The same process is given to all the address lines to complete the read operation for one field. As the result, the interline transfer operation is finished.

Fig.5B shows a state in which a signal charge from one of the diodes, for example a diode 20m, is read in the corresponding vertical CCD 22 (as shown in Fig. 4). In this case, a potential well where the signal charge is stored is called a signal potential well 35; and other wells where signal charges are not stored are called smear potential wells 36

In such a situation, for example, when a strong light is introduced onto a diode 20m + 1, and it is detected by an output signal value for the previous frame or field from the solid-state area sensor 12 that the picture element output is saturated, an electron shutter operation is given to the diode 20m + 1. As shown in Fig. 5C, when the smear potential well 36 in which a signal charge is not stored reaches a position corresponding to the diode 20m + 1, a stop gate line for the diode 20m + 1 is selectively turned on to apply voltage to an address line 23m + 1 so as to exhaust a charge stored into the corresponding vertical CCD. This operation is carried out at a suitable time before a signal read pulse for the real purpose is given to the address line 23m + 1 during the read operation of the solid-state area sensor 22.

Next, when the signal read pulse is applied to the address line 23m + 1 in accordance with a read timing for the real purpose as shown in Fig. 5D, a charge stored again in the diode 20m + 1 is transferred as a signal charge to the signal potential well 35. While, the unnecessary charge exhausted to the smear potential well 36 as mentioned above is exhausted through the gate 27 set at the on state by operation of a drive line 29m + 1 when the smear potential well reaches a position corresponding to the drain 26.

As the result, the sensitivity of the photodiode 20m is kept at an ordinary level, while that of the photodiode 20m + 1 is suppressed by the so-called electron shutter operation.

Fig.6 shows examples of pulses to be applied to each the read gate and each the stop gate. Since a pulse to be given to the read gate is supplied by the vertical shift register 23 shown in Fig. 3, pulses to be supplied therefrom to the photodiodes 20m and 20m 1 include a read timing pulse 40 and an exhaustion timing pulse 41 respectively. In case of the photodiode 20m not to be subjected to the electron shutter operation, a pulse including only a read pulse 42 is given to the corresponding stop gate at the same timing as the read timing pulse 40. At the time, a charge obtained by photoelectric conversion for an interval t0 defined by the length between the two read pulses 42 is read as a signal charge. While, to the stop gate of the photodiode 20m + 1 is given a pulse including the read pulse 42 and an exhaustion pulse 43 respectively at the same timing as the read timing pulse 40 and the exhaustion timing pulse 41 in the pulse to be given to the corresponding read gate.

In this case, since a charge obtained by photoelectric conversion for an interval t2 defined by the length between the read pulse 42 and the exhaustion pulse 43 is exhausted, a signal charge is not given. Accordingly, the exposure time for the

photodiode 20m + 1 is reduced to t2/t0 as compared to that for the photodiode 20m, thus the sensitivity of the former diode 20m + 1 is substantially suppressed.

With respect to the position detection for the sensitivity suppression, it is possible to set a suppression area based on an output of the previous frame or field in the solid-state area sensor 12, which is stored in a memory at a time as described above, or to set a suppression area based on an output signal value for the previous line. Namely, as shown in Fig. 7, when a light object to be photographed is located at the right upper portion in a playback picture plane, a position in a line + 1, where the sensitivity is to be suppressed, is set at the same position as a position receiving a strong incident light in a line j previously shifted by one line from the line j + 1. Otherwise, as shown by using lines k and k 1, it is possible to set an area to be subjected to the sensitivity suppression in the line k + 1 at an area a little wider than an area receiving a strong light in the line k.

Various modification will become possible for those skilled in the art after receiving the teachings of the present disclosure without departing from the scope thereof.

Reference signs in the claims are intended for better understanding and shall not limit the scope.

Claims

1. A solid-state camera comprising:
a solid-state image sensor element (12) composed of a plurality of units of picture element cells (20) planarly arranged therein for outputting electric signals obtained by photoelectric conversion of incident light;
drive means(13) for driving the solid-state image sensor element(12); and
signal process means(14) for processing the signals outputted from the solid-state image sensor element(12) to obtain picture image signals;
wherein the signal process means(14) having means(142) for detecting a position to receive strong light over a predetermined strength introduced onto the solid-state image sensor element(12) and generating a detection signal;
and
the drive means(13) having sensitivity adjustment means(132) for outputting sensitivity adjustment signals(133) to adjust sensitivity of each the unit of picture element cells(20) existing in a specific portion in the solid-state image sensor element(12) based on the detection signal.
2. A solid-state camera according to claim 1, wherein the solid-state image sensor element(12) is composed of a line address type CCD sensor

comprising vertical CCD(22) and horizontal CCD(24);

the sensitivity adjustment means(132) has an electron shutter function; and

sensitivity of each the unit of the picture element cells (20) is adjusted by the sensitivity adjustment means (132) based on the electron shutter function thereof.

3. A solid-state camera according to claim 2, wherein the electron shutter function of the sensitivity adjustment means(132) includes read gates(21) provided corresponding to the respective picture element cells(20), stop gates(28) respectively connected to the read gates(21) in series, and exhaustion drains(26) respectively connected to the stop gates(28) in series and provided at end portions of the vertical CCD(22) so as to exhaust output signals.

4. A solid-state camera according to claim 3, wherein each the stop gate(28) is respectively opened at the same timings as a read pulse and an exhaustion pulse for exhausting the output signal to each the exhaustion drain(26), both of which are supplied to each the read gate(21), in the position to receive strong light over a predetermined strength.

5. A solid-state camera according to claim 3, wherein each the read gate(21) is composed of a MOS transistor.

6. A solid-state camera according to claim 2, wherein picture image signals from each the unit of picture element cells (20) in an area where the sensitivity is to be adjusted by the sensitivity adjustment means(132) are adjusted by the sensitivity adjustment means(132) based on the output from the picture element cells(20) of the previous line in the solid-state image sensor element(12).

7. A solid-state camera according to claim 2, wherein picture image signals from each the unit of picture element cells(20) in an area where the sensitivity is to be adjusted by the sensitivity adjustment means(132) are adjusted by the sensitivity adjustment means(132) based on the output from the picture element cells(20) of the previous field in the solid-state image sensor element(12).

8. A signal processing method of a solid-state camera comprising:

a step of outputting electric signals generated by photoelectric conversion of incident light received by a solid-state image sensor element(12) composed of a plurality of planarly arranged picture element cells(20);

a step of processing an output signal from a unit of picture element cells(20) of a previous field by an output signal process means(14), and detecting a position of a unit of picture element cells(20) in the solid-state image sensor element(12) corresponding to the output signal when the output signal is

over a predetermined value, then generating a detection signal;

a step of outputting a sensitivity adjustment signal to the unit of picture element cells(20) based on the detection signal; and a step of controlling the output signal of the unit of picture element cells(20) to be an object based on the sensitivity adjustment signal.

9. A signal processing method of a solid-state camera comprising:

a step of outputting electric signals generated by photoelectric conversion of incident light received by a solid-state image sensor element(12) composed of a plurality of planarly arranged picture element cells(20);

a step of processing an output signal from a unit of picture element cells(20) of a previous line by an output signal process means(14), and detecting a position of a unit of picture element cells(20) in the solid-state image sensor element(12) corresponding to the output signal when the output signal is over a predetermined value, then generating a detection signal;

a step of outputting a sensitivity adjustment signal to the unit of picture element cells(20) based on the detection signal; and

a step of controlling the output signal of the unit of picture element cells (20) to be an object based on the sensitivity adjustment signal.

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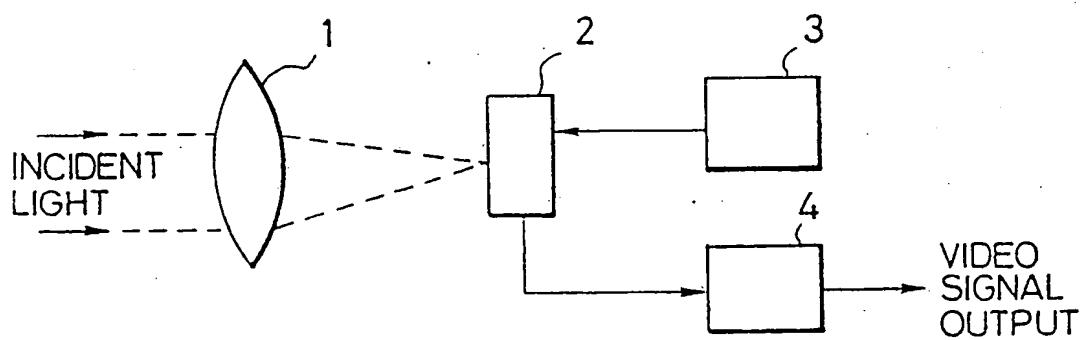
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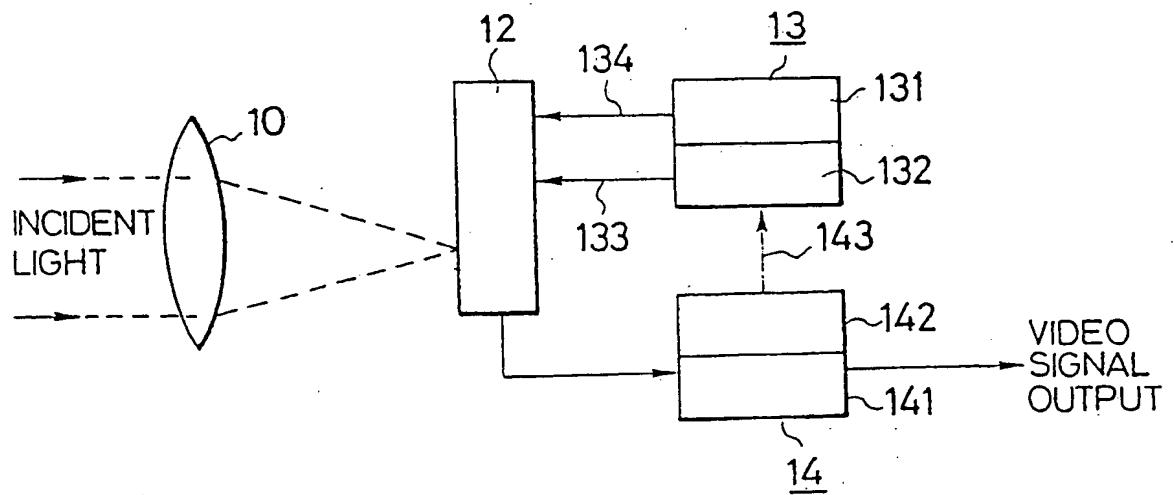
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F I G. 1



F I G. 2



F I G. 3

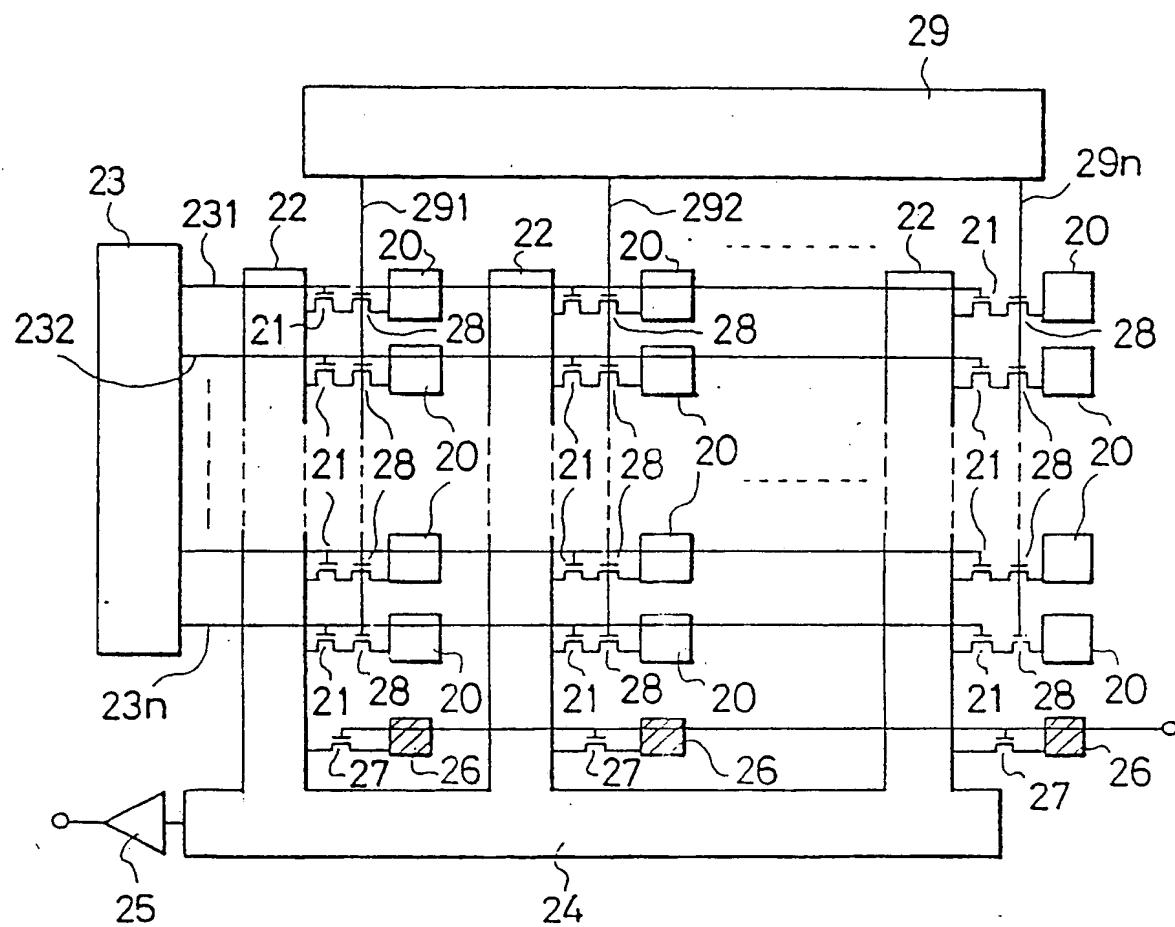


FIG. 4

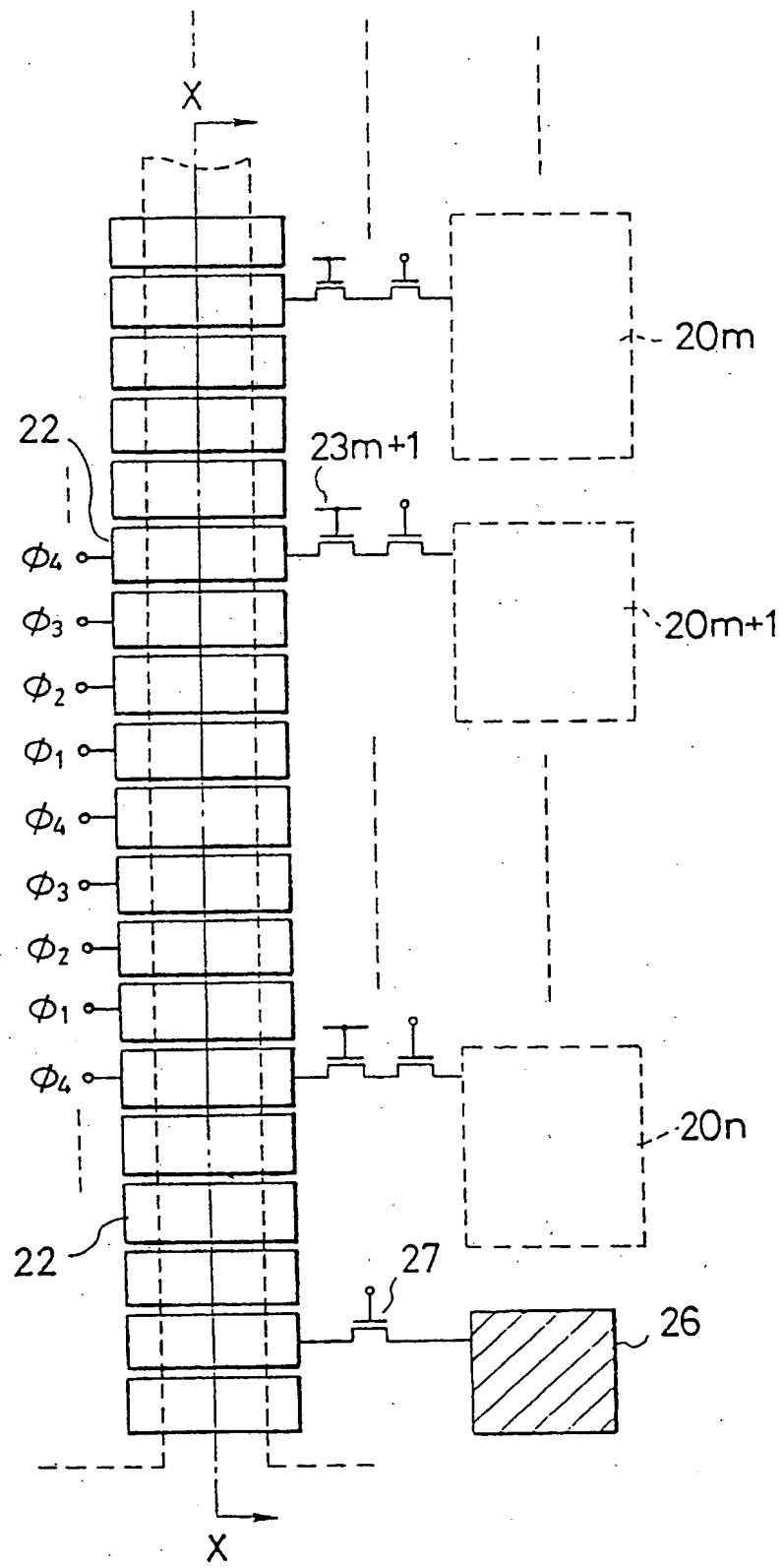


FIG. 5 A

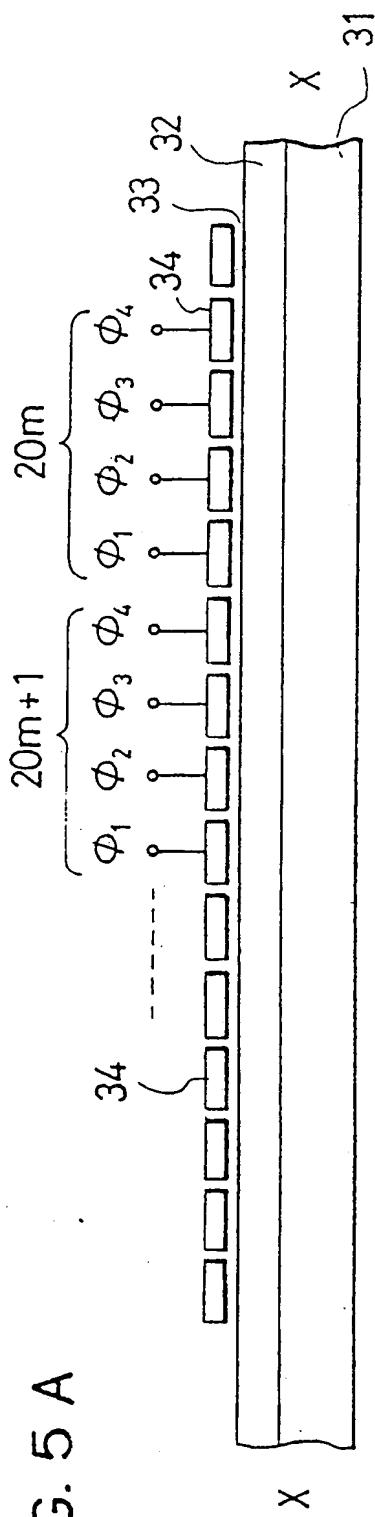


FIG. 5 B

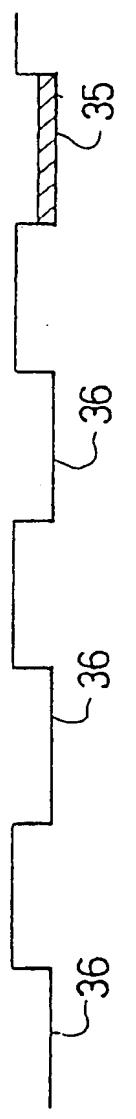


FIG. 5 C

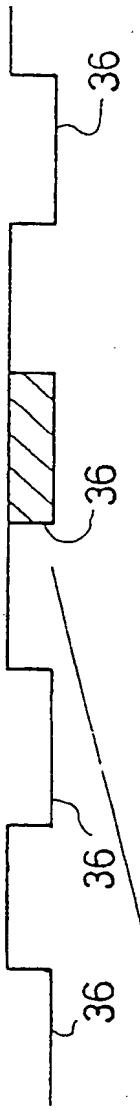
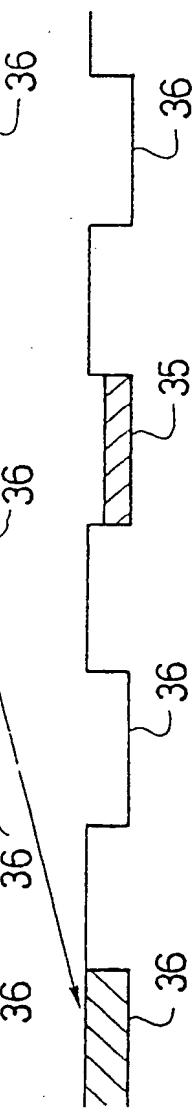


FIG. 5 D



→ TRANSFER DIRECTION

FIG. 6

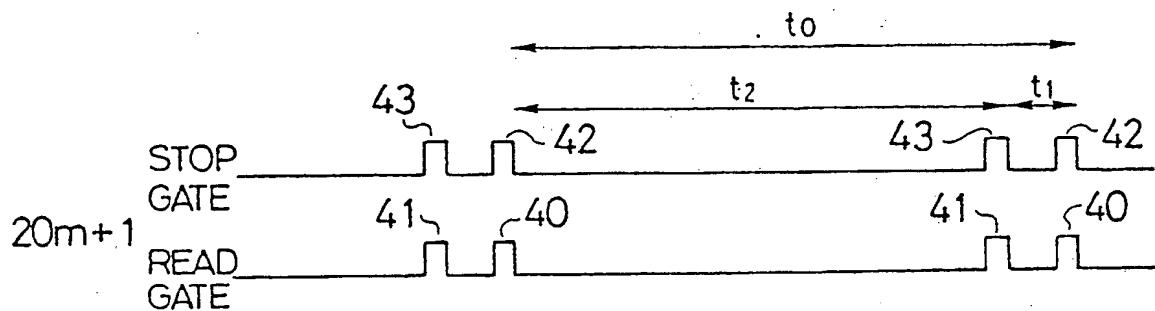
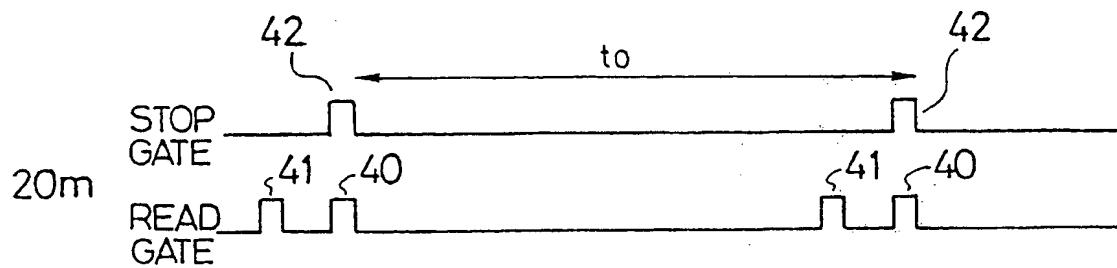
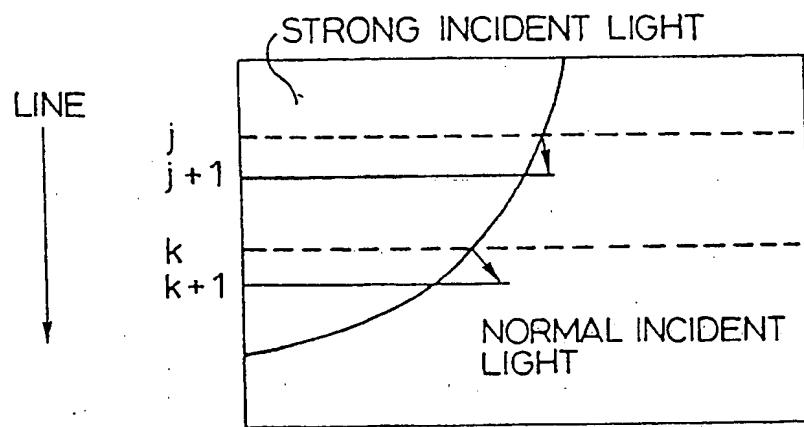


FIG. 7





European Patent
Office

EUROPEAN SEARCH REPORT

Application Number

DOCUMENTS CONSIDERED TO BE RELEVANT			EP 90109663.6
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
A	<u>US - A - 4 866 292</u> (TAKEMOTO) * Fig. 1,3; column 2, line 33 - column 10, line 21 * --	1,2,3 5,8,9	H 04 N 3/15 H 04 N 5/335 H 04 N 5/21
A	<u>US - A - 4 835 617</u> (TODAKA) * Fig. 1,3; column 2, line 63 - column 5, line 18 * --	1,2,3 5,8,9	
A	<u>US - A - 4 628 364</u> (KOCH) * Fig. 1; column 2, line 20 - column 4, line 23 * --	1,3,8 9	
A	<u>US - A - 4 800 435</u> (IKEDA) * Fig. 1A,1B; column 3, line 4 - column 4, line 58; fig. 8; column 8, line 63 - column 9, line 15 * --	1,2,8 9	
A	<u>US - A - 4 764 814</u> (ENDO) * Fig. 2; column 3, line 57 - column 4, line 17 * ----	1,2	H 04 N 3/00 H 04 N 5/00
THE PRESENT SEARCH REPORT HAS BEEN DRAWN UP FOR ALL CLAIMS			
Place of search	Date of completion of the search	Examiner	
VIENNA	15-11-1990	BENISCHKA	
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	